



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Yasuro MatsuZAKI

Serial No.: 10/003,048

Filed: December 6, 2001

For: INPUT/OUTPUT INTERFACE AND SEMICONDUCTOR INTEGRATED  
CIRCUIT HAVING INPUT/OUTPUT INTERFACE

Confirmation No: 4931

Art Unit: 2819

Examiner: Vibol Tan

Docket. No.: 108397-00052

RESPONSE

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

September 8, 2003

Sir:

In response to the outstanding Office Action dated June 11, 2003, (hereinafter  
"outstanding Action") with the response time falling within the shortened statutory period,  
please reconsider the above-identified application in view of the following remarks:

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TECHNOLOGY CENTER 2800